

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	86	flash same ((compare or compares\$3 or comparator\$2) and (row adj3 address) and operation and (un\$1lock\$1 or lock\$2) and chip and (control adj2 circuit) and detect\$3) and register and latch\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/01 12:59
L2	3	1 same (row adj3 address) same operation	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/01 13:00
L3	15	1 and (row adj3 address) and operation and (control adj3 circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/01 13:04
L4	13	3 and detect\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/01 13:00
L5	10	4 and "365"/\$7.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/01 13:02
L8	1	3 and 365/195.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/01 13:04
L10	142	(flash and (compare or compares\$3 or comparator\$2) and (row adj3 address) and operation and (un\$1lock\$1 or lock\$2) and chip and (control adj2 circuit) and detect\$3) and register and latch\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/01 13:06
L11	1	(flash and (compare or compares\$3 or comparator\$2) and (row adj3 address) and operation and (un\$1lock\$1 or lock\$2) and chip and (control adj2 circuit) and detect\$3).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/01 13:11

L12	4	10 and 365/195.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/01 13:07
L14	18	10 and 365/195,189.07,189.05, 189.12.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/01 13:55
L16	0	(flash and array and (control adj3 circuit) and (control adj3 signal) and bit and word and address and (compare or compares\$3 or comparator\$2) and (lock\$2 or unlock\$2)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/01 13:11
L17	1	(flash and load\$3 and address and unlock\$2 and pulse\$2 and (sync or clock\$3) and (compare\$3 or compare or comparator\$2 or compares\$3) and (erase or (program or programmable\$3))). clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/01 13:15
L18	0	(flash and chip and array and address and operation and unlock\$2 and (control adj3 circuit) and (compare\$3 or compare or compares\$3 or comparator\$2) and first and second).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/01 13:17
L19	11	"6209069"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/01 13:57
L20	14	"6031757"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/01 14:00
L21	27	"5513136"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/01 14:04

L22	25	"5363334"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/01 14:04
L23	14	"5345413"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/01 14:05
L24	44	"5197034"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/01 14:07
L25	0	byeon-dae.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/01 13:56
L26	0	byeon-dae-s.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/01 13:56
L27	21	byeon-dae-\$.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/01 13:56
L28	0	do-kyunki.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/01 13:56
L29	0	do-kyunki-\$.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/01 13:56

L30	5	lee-seung-j.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/01 13:57
L31	5	lee-seung.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/01 13:56
L32	0	lee-seung-4.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/01 13:56
L35	3	flash same memory same (compare or compar\$3 or comparesion) same (address or addressabl\$2) same (control or controller or controll\$3) same (detect or detect\$3) same un\$1lock\$2.ab.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/01 14:04